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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of

GILBERT P. HYATT

Serial No. 08/464,034

Docket No. 751

Filed: June 5, 1995

For: IMPROVED IMAGE PROCESSING
ARCHITECTURE

GROUP 260

Group Art Unit: Not yet assigned

Examiner: Not yet assigned

PRELIMINARY AMENDMENT

Hon. Assistant Commissioner
For Patents
Washington, D.C. 20231

Sir:

Cancel claims 1-20 and substitute in place thereof claims
21-97 as follows.

--21. An image processing system comprising:
a processor generating image information;
a plurality of memories including a first memory, a
second memory, and a third memory;
an input circuit coupled to the first memory and
writing a sequence of input words into the first memory in
response to the image information;
a first output circuit coupled to the second memory and
outputting a first sequence of output words from the second
memory simultaneously with the writing of the sequence of input
words into the first memory by the input circuit; and
a second output circuit coupled to the third memory and
outputting a second sequence of output words from the third
memory simultaneously with the writing of the sequence of input
words into the first memory by the input circuit and
simultaneously with the outputting of the first sequence of
output words from the second memory by the first output circuit.

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